Micron interview questions (nidhi)

1. Introduction.

2. Difference Between Verilog and System Verilog .

3. Role in project.

4. Test cases worked on .

5. Bugs Identified During Testing.

6. VAMS Testbench Overview.

7. What challenges faced during the simulation.

8. Draw and explain circuit diagram of Buck Regulator.

9. Draw and explain circuit diagram of LDO.

10. What will happen or what changed we have to do if we get higher value on the LDO output than expected .

11. What is Mosfet .

12. Draw and explain NMOS.

13. Characteristics of Mosfet.

14. What is body effect .

15. What happens if negative voltage is applied to the body terminal of NMOS.

16. What is channel length modulation .

17. RC low pass filter output waveform of voltage with respect to time and frequency.

18. What is RC time constant .

19. How to calculate voltage across resistor using voltage divider rule.

20. What is convergence issue how to avoid it .

21. How will we provide leakage current path to a floating node.

22. How to increase the simulation time .

23. What is Gmin and Cmin , why they are used .

24. eerpreset and Techniques to Improve Accuracy

25. Gave a specification of buck regulator to read and asked to explain it to him.

Micron Interview Questions – AMS (tejas)

1. Introduction

2. What you did as verification in your project

3. What were the bugs found

4. How do we increase accuracy of the tool

5. What are trap, gear0, gear1, etc

6. What is gmin, cmin, etc

7. Characteristics diagram of nMos

8. In which region nMos acts as an amplifier

9. As in saturation region according graph Id drain current becomes constant, so how nMos acts as an amplifier in saturation region ?

11. What is Body effect ?

12. If charge density is increased then how Vth is increased and how channel becomes wider ?

13. CMOS as an inverter

14. What happens if body terminal of pmos in this CMOS is connected to ground

15. What are transmission gates

16. Gave a RC circuit and asked to generate its waveform

17. What is current mirror circuit ? Why it is used ?

18. Showed a circuit and asked identify which circuit it is. (Circuit consists of multiple current mirror circuits)

19. Gave waveforms and asked to write wreal model to generate these waveform.